

indicates a voltage is at a minimum current voltage level when the current drawn is at a minimum but nonzero load current level (*see, e.g.*, claim 1).

Claims 1-16 were rejected under 35 U.S.C. § 102(e) as being fully anticipated by Yang et al. (U.S. Patent No. 6,130,526).

The Examiner has also failed to show in any office action how the cited Yang contain any specific elements corresponding to claimed elements of the present invention. More specifically, the Examiner has again failed to show that the adjusting element or module of the pending claims that indicates a voltage is at a minimum current voltage level when the current drawn is at a minimum but nonzero load current level.

Claims 1 and 7-9 were rejected under 35 U.S.C. § 102(a) as being fully anticipated by applicant's admitted prior art as shown in Figures 1 and 2. The Examiner's attention is again drawn to the voltage legends on Figures 1, 2, 4, and 5. In Figures 1 and 2, a voltage $V_I(0)$ is shown, corresponding to a no-current voltage level (*see, e.g.*, the specification, p. 4, ln. 15). In Figures 4 and 5, a voltage $V_I(k)$ is instead shown, corresponding to a low but nonzero current output $I(k)$ (*see, e.g.*, the specification, p. 5, ln. 4-6. For further clarification of the differences between the no-current condition $I(0)$ and the low current condition $I(k)$, the Examiner's attention is drawn to Figure 3 and the accompanying description in the specification.

Because anticipation under 35 U.S.C. § 102 requires the disclosure in a single prior art reference of each element of the claim under consideration (*In re Dillon* 919 F.2d 688, 16 USPQ2d 1897, 1908 (Fed. Cir. 1990) (en banc), cert. denied, 500 U.S. 904 (1991)), and such anticipation has not been shown, the present claims are believed to stand in condition for allowance. Further, § 102 anticipation requires that "The identical invention must be shown in as complete detail as is contained in the ... claim." *Richardson v. Suzuki Motor Co.*, 868 F.2d 1226, 1236, 9 USPQ2d 1913, 1920 (Fed. Cir. 1989); MPEP § 2131. Such detail has not been shown here.

Conclusion

Applicant therefore respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney (612) 349-9581 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Box AF, Commissioner of Patents, Washington, D.C. 20231, on this 14 day of June, 2001.

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